

AMENDMENTS TO THE SPECIFICATION:

Please replace paragraph [00135] with the following amended paragraph:

[00135] According to one embodiment of the present invention, a bus interface device is responsible for managing all transfers between the processor and the system bus. In Figure 9, a block diagram of this interface device 900 is shown. This interface device and methodology are the subject of copending patent application Serial No. 09/955,966, (Attorney Docket No. 017750-413) filed on even date herewith, entitled "System Bus Transceiver Interface," which is hereby incorporated by reference in its entirety herein. The major function of the bus interface shown in the block diagram 900 is to utilize a transmitter and receiver that operate between the local processor bus 902 and the system bus or module bus 904. The bus 904 may be either the system bus 102 or the module bus 122, shown in Figure 1. The local processor bus 902 is the bus connecting the bus interface device 128 to components within the same node of the module, such as processors and memory, or, in the case of the sensor interface, internal processors and memory. The interface illustrated in the block diagram 900 of Figure 9 provides the necessary functions to meet the requirements of the system illustrated in Figure 1, for example. This bus interface also includes support DMA operations and control actions, as used in embodiments of the present invention.